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CASE DOCKET NO.YOR920030438US1 Date: November 13, 2003

Sir:

Transmitted herewith for filing is the Patent Application of:

Inventors: V. Narayanan, et al

For: CVD Tantalum compounds for FET gate electrodes

Enclosed are:

Expresss Mail Label No.: ER459992766US Date Deposited: November 13, 2003

_X__ 7 Sheets of Formal Drawings.

This application is filed Under Rule 1.53(b) X

The filing fee has been calculated as shown below:

(Col. 1) (Col. 2) FOR: NO. FILED NO. EXTRA BASIC FEE TOTAL CLAIMS 35 -20 =15 INDEP CLAIMS 3 = 1 O MULTIPLE DEPENDENT CLAIM PRESENTED

If the difference in Col. 1 is less than zero, enter "0" in Col. 2.

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Respectfully submitted,

Mgent: George Sai-Halasz Registration No.: 45,430

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145 Fernwood Dr.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Vijay Narayanan et al.

Serial No.:

Group Art Unit:

Filed: herewith

Examiner:

For: CVD Tantalum compounds for FET gate electrodes

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

EXPRESS MAIL CERTIFICATE

"Express Mail" label number: ER459992766US

Date of Deposit: November 13, 2003

I hereby certify that the following attached paper or fee

Patent Application (19 sheets) filed under Rule 1.53(b)

Seven (7) sheets of formal drawings

Declaration and Power of Attorney (7 pages, 2 copies of page 2, 3 copies of page 4)

Associate Power of Attorney

Assignment (7 pages, 2 copies of page 3, 3 copies of page 4)

Recordation form

Patent Transmittal Sheet (in duplicate)

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